

Scholar

[Articles and patents](#)[anytime](#)[include citations](#)[Create email alert](#)

Results 1 - 10 of about 2,080. (0.17 sec)

Repetitive control for systems with uncertain period-time* 1[tue.nl](#) [PDF]

M Steinbuch - Automatica, 2002 - Elsevier

... in period-time (Section 3). The robust repetitive **controller** will be experimentally tested for the **servo** control of ... can be modelled using an integrator, and inclusion of integral action in the feedback **controller** prevents steady ... a signal with period $T_p = 2\pi/\omega_p$ is stored in a **FIFO** buffer ...

[Cited by 77](#) - [Related articles](#) - [All 11 versions](#)[PDF] Small Adaptive Flight Control Systems for UAVs using FPGA/DSP Technology[psu.edu](#) [PDF]

HB Christophersen, WJ Pickell, AA Koller, SK ... - American Institute of ... - Citeseer

... Attitude Control System (YACS) **interface** that allows raw **servo** commands to be given without modification by this ... **Servo Controller Servo Controller Servo Controller Servo Controller** ... 8. The Nios CPU in the FPGA uses rate gyro data to adjust the pilot's **servo** control signals in ...

[Cited by 9](#) - [Related articles](#) - [View as HTML](#) - [All 9 versions](#)[PDF] PC-based open architecture servo controller for CNC machining[linuxdevices.com](#) [PDF]

S Kommareddy, Y Kazuo, K ... - The Second Real Time ... , 2000 - linuxdevices.com

... these objectives in view, a platform was designed to implement a real-time **servo** control system ... **Interface** (HMI) has been chosen as the method of obtaining HMI-GUI functionality for the **controller**. Since Windows NT is the front-end and it has to **interface** with the RT-Linux ...

[Cited by 5](#) - [Related articles](#) - [View as HTML](#) - [All 9 versions](#)[PDF] An efficient bus architecture for system-on-chip design[nsysu.edu.tw](#) [PDF]

S Cordan - Proceedings of the IEEE Custom Integrated ... , 1999 - eslab.cse.nsysu.edu.tw

... Flash Figure 3 Variations may be accomplished by replacing the ATA with a 1394 or PC-Card **interface**; upgrading the **servo** logic or ... The example shown here has been implemented in silicon as PALMCHIP's GreenLite™ HDD **Controller** and has seen at least five ...

[Cited by 53](#) - [Related articles](#) - [BL Direct](#) - [All 8 versions](#)Hardware tracing/logging for highly integrated embedded controller device

NC Assouad, DL Dyer, W Lin - US Patent 6,119,254, 2000 - Google Patents

... in much the same manner except that the signals received by disk **interface/ECC controller** 203 and **servo controller** 207 cause ... **interface controller** 201 which comprises host **interface** control logic 310 and its associated host **fifo** 308, disk **interface/ECC controller** 203 and ...

[Cited by 7](#) - [Related articles](#)Servo pause for disk drive embedded multi-tasked controller

DB Jeppson, NH Wulferdinger, BJ Mee, WN ... - US Patent ... , 1994 - Google Patents

... Data blocks are passed between the **interface** 60, cache buffer RAM and the encoder-decoder 44 ... The **controller** 66 receives phase commutation values from the motor 14 and passes motor speed ... A spindle clock signal from the **servo** interface 46 provides a reference to which ...

[Cited by 6](#) - [Related articles](#)An undergraduate system-on-chip (SoC) course for computer engineering students[sisu.edu](#) [PDF]

A Bindal, S Mann, BN Ahmed, LA ... - IEEE Transactions on ... , 2005 - ieeexplore.ieee.org

... 5. In this figure, the user data packet carrying the new **servo** ID and position first arrives to the UART **FIFO** in Excalibur's Stripe ... The **servo controller interface** is responsible for translating the AMBA address and control signals to the address and controls of the **servo controller** in ...

[Cited by 12](#) - [Related articles](#) - [All 3 versions](#)On-the-fly error detection and correction buffer processor

M Ou, LE Adams, SJA Zaidi, HI Ramlaoui - US Patent 5,978,954, 1999 - Google Patents

... digital disk drive **controller** that integrates an advanced RISC-processor micro-**controller**, a host **interface**, a memory access **controller**, a disk-drive **servo-control interface** ... a headerless ID-free formatter **interface**, a Reed- Solomon triple on-the-fly ECC, a serial **interface**, and a ...

[Cited by 4](#) - [Related articles](#)Optimization of multimedia magnetic disk storage devices

TJ Chainer, E Feig - US Patent 6,075,665, 2000 - Google Patents


... actuator 206. The electronic components include a data channel 208, an actuator **servo controller** 210, a spindle speed **controller** 212, a first-in/first out (**FIFO**) memory buffer 214 and a SCSI **interface** 216. The recording transducer ...

[Cited by 4](#) - [Related articles](#)Rotary drum controller

M Yamada, M Noguchi, J Ono, T Takahashi - US Patent 5,231,548, 1993 - Google Patents

... the Phase reference signal D^m are passed through an Finally, the **servo controller** 21 preferably is opera- OR gate 36 to a **FIFO controller** 37. live to generate a drum control signal SDR in accordance with the invention. In response to either pulse P10 or P20, the **FIFO** dance with the invention. ...

[Cited by 3](#) - [Related articles](#)

 [Create email alert](#)

Google 

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [Next](#)

[Go to Google Home](#) - [About Google](#) - [About Google Scholar](#)

©2010 Google